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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant(s)	:	Kazutaka SHIBATA)	
Serial No.	:	Divisional of Appl. Serial No. 10/088,163)	
Filed	:	March 10, 2004)	INFORMATION DISCLOSURE
For	:	SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME)	230000000000000000000000000000000000000
Attornev Ref.	:	KAW 110 D1)	

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, attached are copies of the Forms PTO-1449 from the parent application, serial number, 10/088,163, which the Examiner may wish to consult during examination of this Divisional Application thereof.

Respectfully submitted,

March 10, 2004

Date

Steven M. Rabin (Reg. No. 29.102)

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10/088163 UC13 Rec'd PCT/PTO 15 MAR 2002

OMB No. 0651-0011 (12/31/86)

PAGE 1 OF Serial No. Atty. Docket No. INFORMATION DISCLOSURE CITATION KWM-001 Not yet assigned (Use several sheets if necessary) **Applicant** Kazutaka SHIBATA Filing Date Group Concurrently herewith Not yet assigned U.S. PATENT DOCUMENTS *Examiner Document Date Name Class Subclass Filing Date Number If Appropriate 03/1997 A1 5611481 Akamatsu et al. FOREIGN PATENT DOCUMENTS Document Date Country Class Subclass Translation Number Yes No A2 03-276750 12/1991 **JAPAN** abstract **A3** 10-321671 12/1998 **JAPAN** abstract A4 EP 0766310A2 04/1997 EP OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Page, Etc.) DATE CONSIDERED **EXAMINER** Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-FB-A820 (also form PTO-1449)

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